

ABSTRACT OF THE DISCLOSURE

In a direct conversion receiver with zero-frequency intermediate frequency (IF) signal, the DC offset and $1/f$ noise of the IF signal is compensated by means of double-sampling. The first period of the doubling-sampling is a calibration phase, which stores the DC offset and the $1/f$ noise. The second period is a signal flow phase during which the stored DC offset and $1/f$ noise is connected in opposition with the IF signal to cancel the DC offset and $1/f$ noise.

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